

051005T4ICT

ICT TECHNICIAN LEVEL 5

IT/OS/ICT/CC/01/5

APPLY BASIC ELECTRONIC

NOV/DEC 2023



**TVET CURRICULUM DEVELOPMENT, ASSESSMENT AND CERTIFICATION
COUNCIL (TVET CDACC)**

WRITTEN ASSESSMENT

Time: 3 Hours

INSTRUCTIONS TO CANDIDATE

1. This paper has THREE sections A, B and C. Attempt questions in each section as per instructions given in the section.
2. You are provided with a separate answer booklet.
3. Marks for each question are indicated in the brackets.
4. Do not write on the question paper

This paper consists of 9 printed pages

**Candidates should check the question paper to ascertain that all pages
are printed as indicated and that no questions are missing**

SECTION A: 20 MARKS

Answer ALL the questions in this section.

Each question carries one Mark.

1. Which of the following is an application of Zener diode?
 - A. Rectifier
 - B. Voltage regulator
 - C. Amplifier
 - D. Oscillator
2. What happens to the width of the depletion region when a PN junction is forward biased?
 - A. It increases.
 - B. It remains the same.
 - C. It decreases.
 - D. It diminishes
3. During a practical lesson in Matyra TVC, level 5 learners were tasked with making an integrated circuit (IC). Which of the following material did they use?
 - A. Silicon
 - B. Germanium
 - C. Copper
 - D. Glass
4. When a Semi-Conductor material is heated, it's resistance _____
 - A. Decreases
 - B. Increases
 - C. Does not change
 - D. Increase then Decreases
5. In digital logic circuits, there are several types of gates, each with its unique behavior when inputs are connected in different ways. What is the resulting circuit when inputs of a NAND gate are connected together.
 - A. OR gate
 - B. AND gate
 - C. NOT gate
 - D. XOR gate
6. Babu just finished measuring electric power and is recording his results. The rate of change in electric power was measured in?
 - A. Joules